

Fig.1a
(prior art)

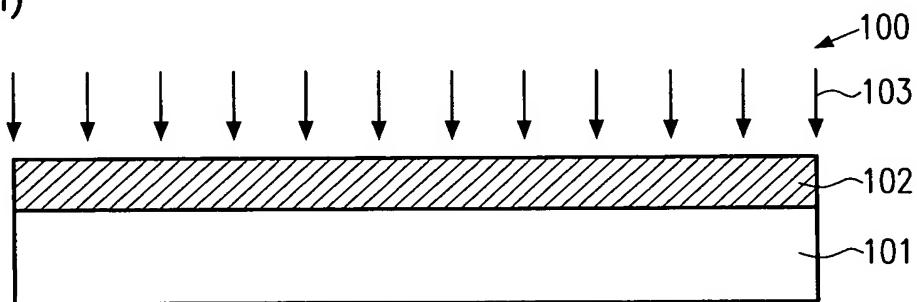


Fig.1b
(prior art)

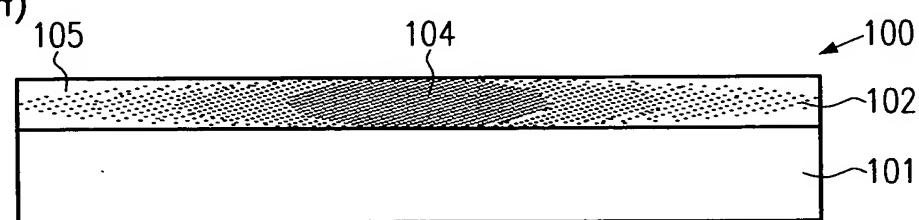


Fig.1c
(prior art)

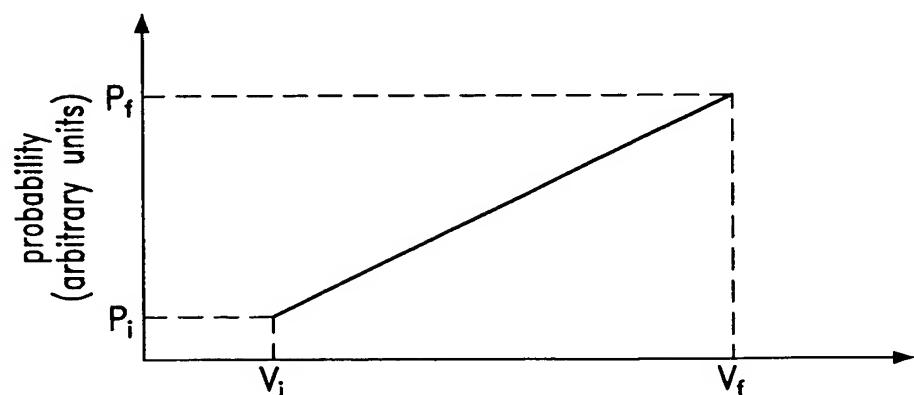


Fig.1d
(prior art)

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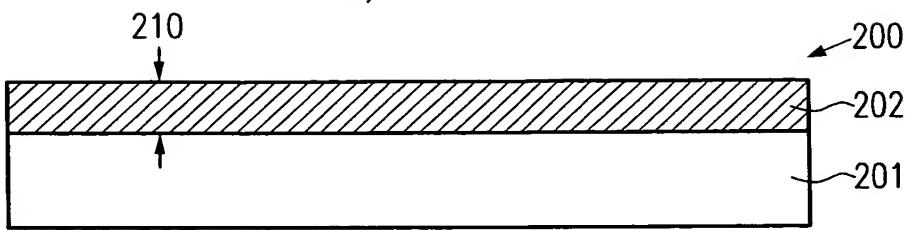


Fig.2a

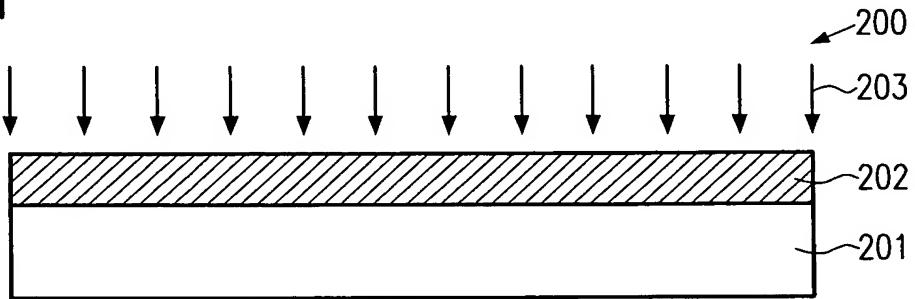


Fig.2b

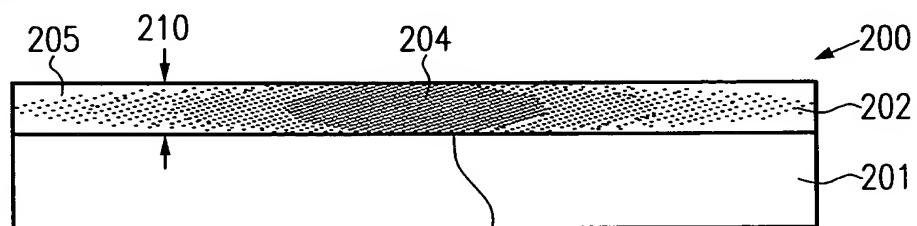


Fig.2c

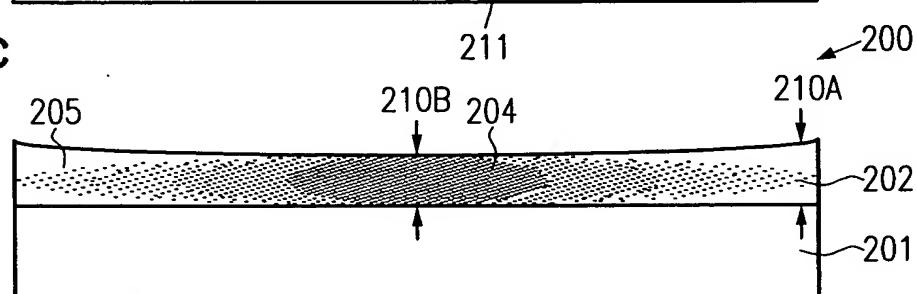


Fig.2d

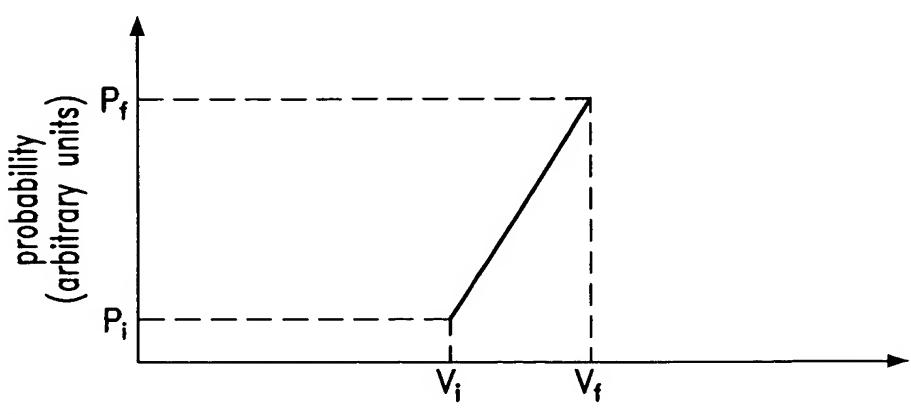


Fig.2e

PMOS transistor threshold voltage
(arbitrary units)